

Fig. 2A
Prior Art

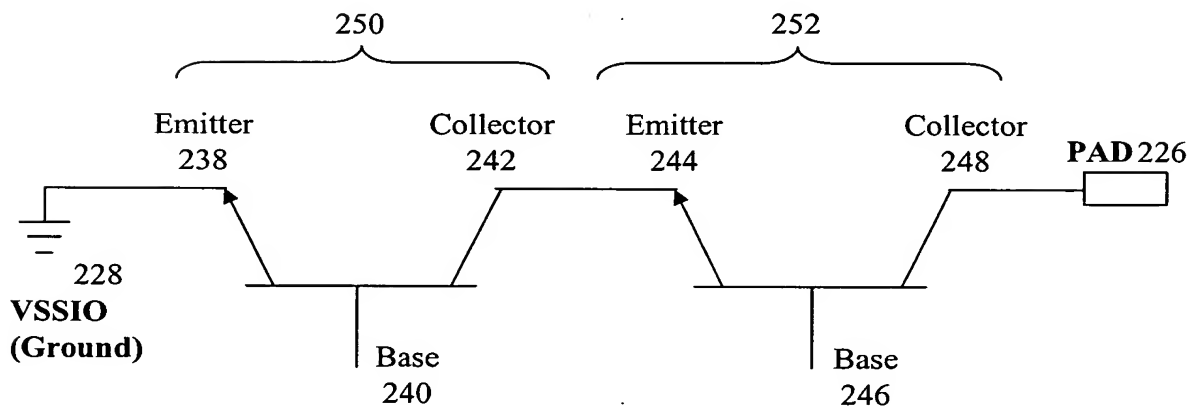


Fig. 2B
Prior Art

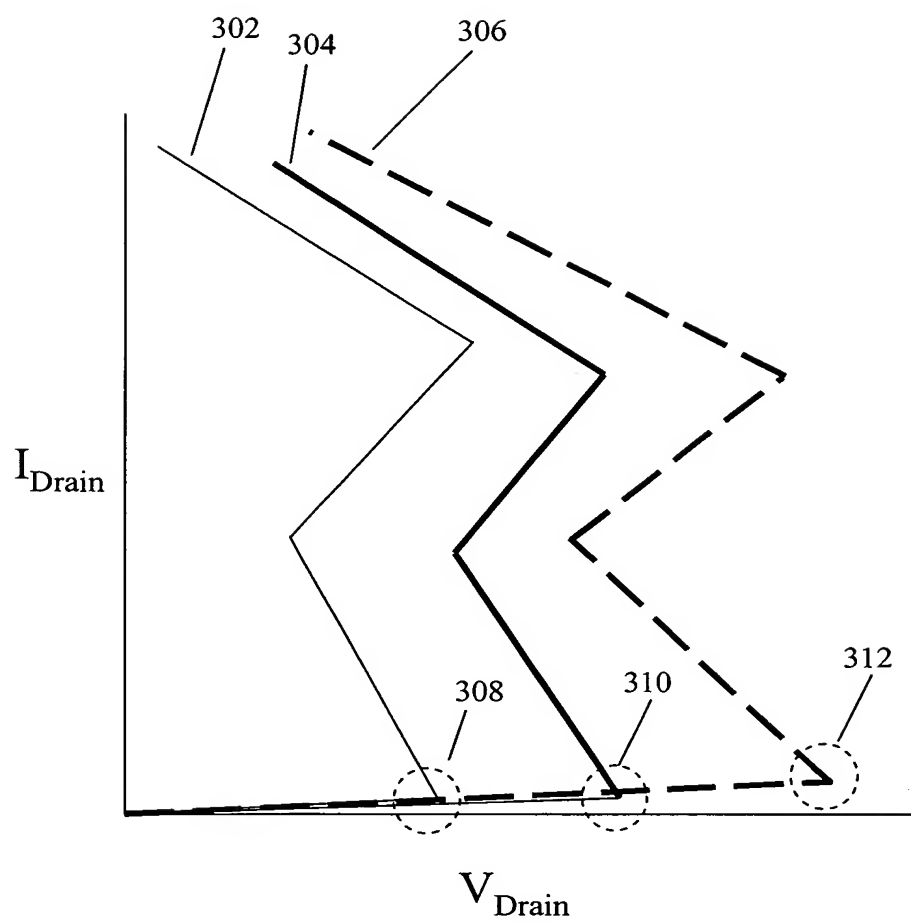


Fig. 3

Silicon	<u>406</u>
BOX	<u>404</u>
Silicon Substrate	<u>402</u>

Fig. 4A

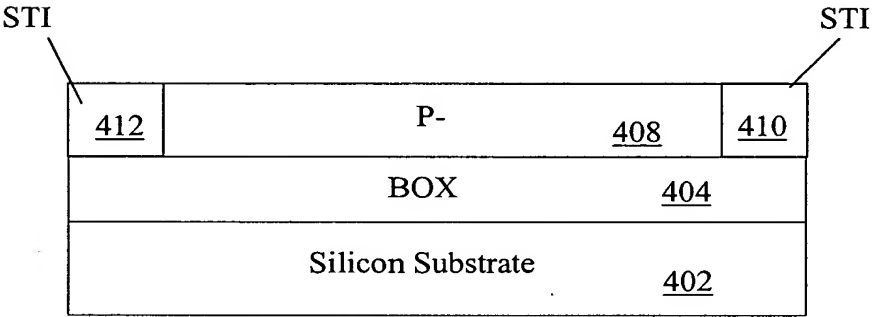


Fig. 4B

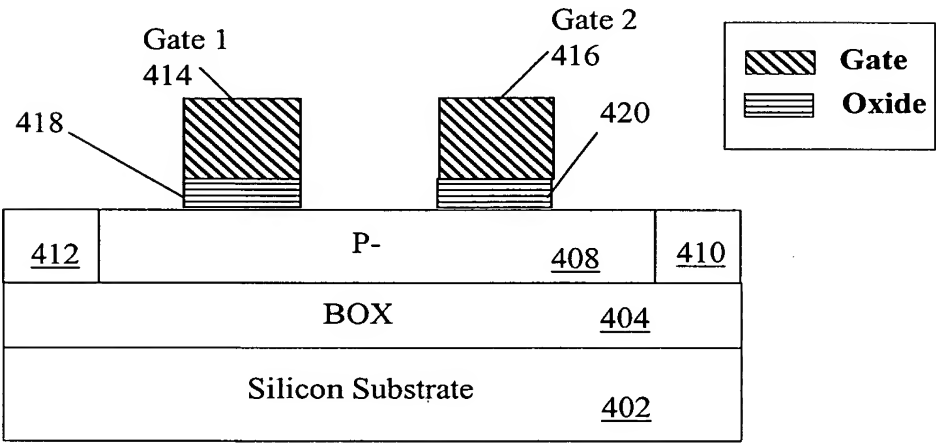


Fig. 4C

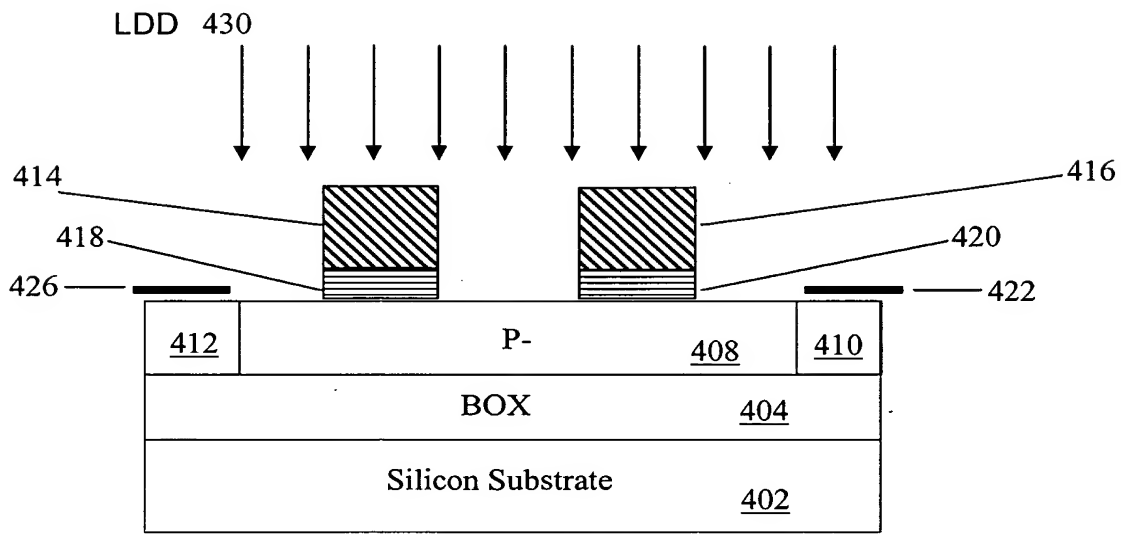


Fig. 4D

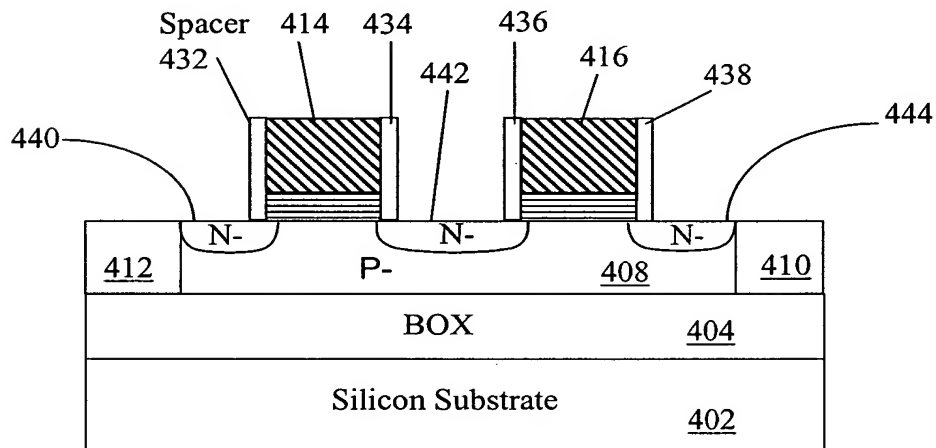
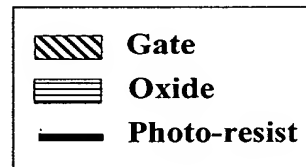
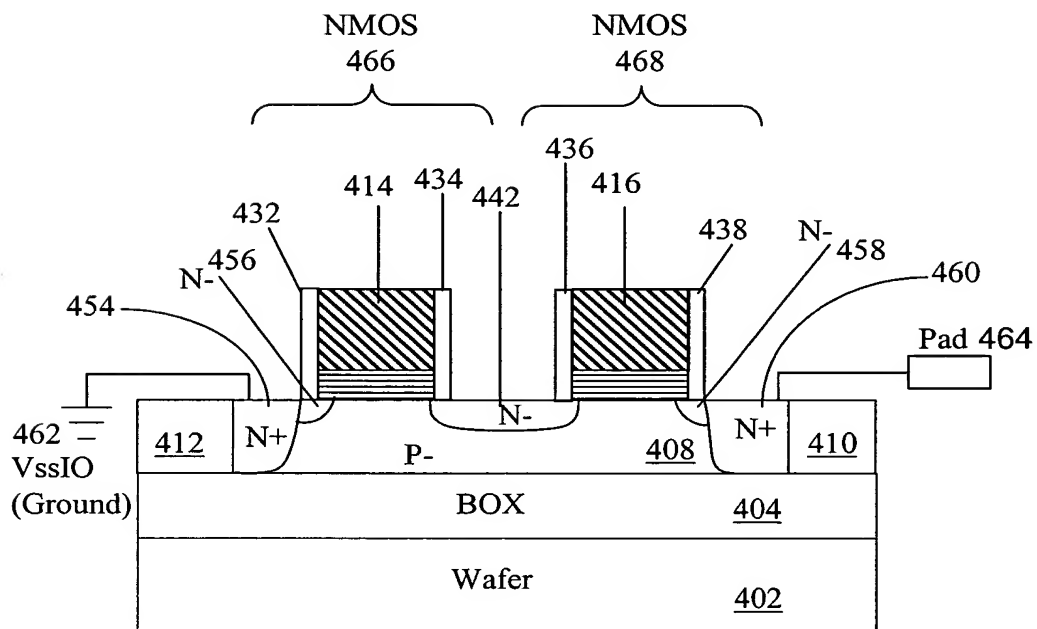
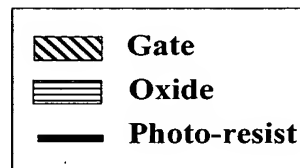
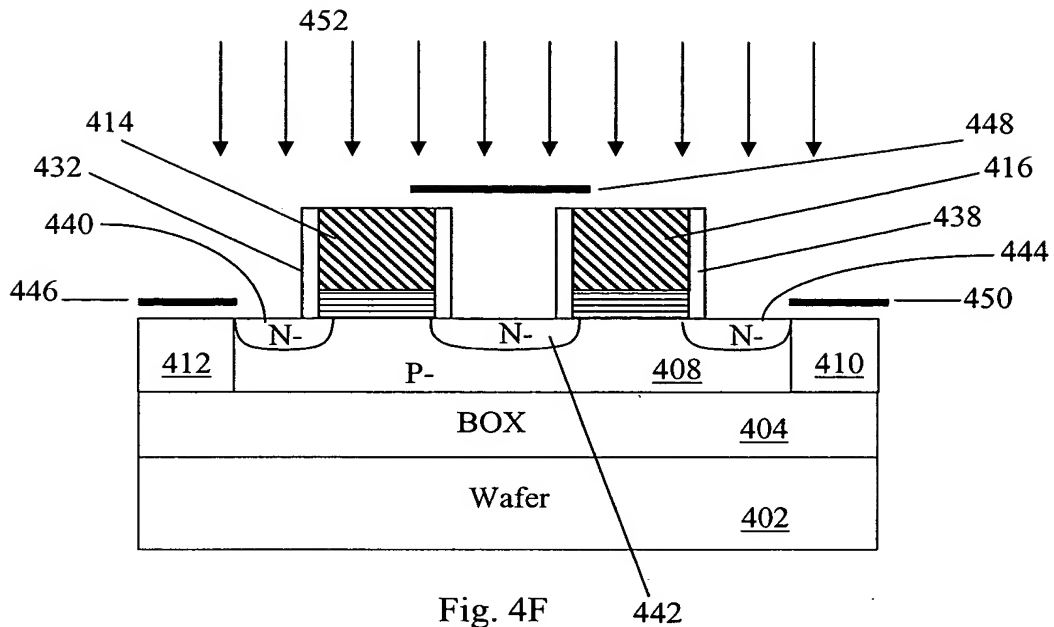


Fig. 4E



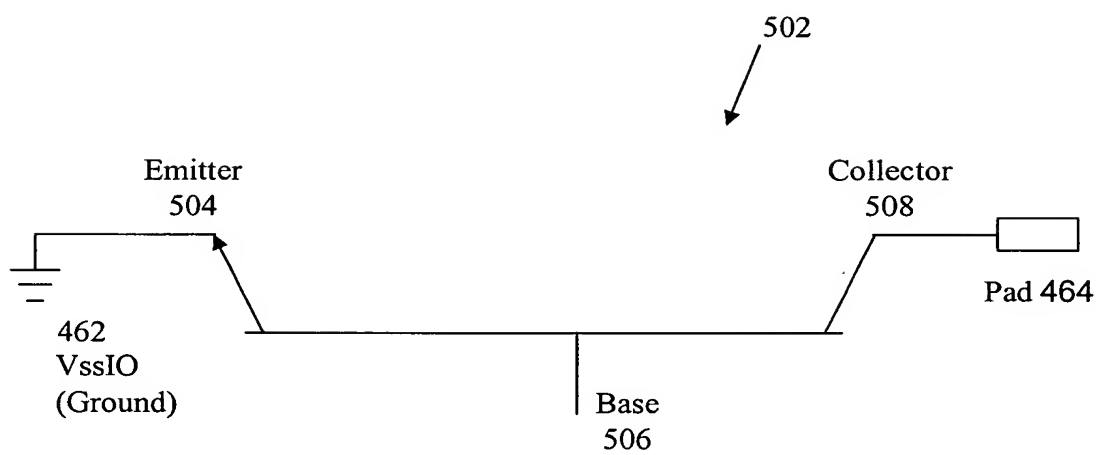


Fig. 5

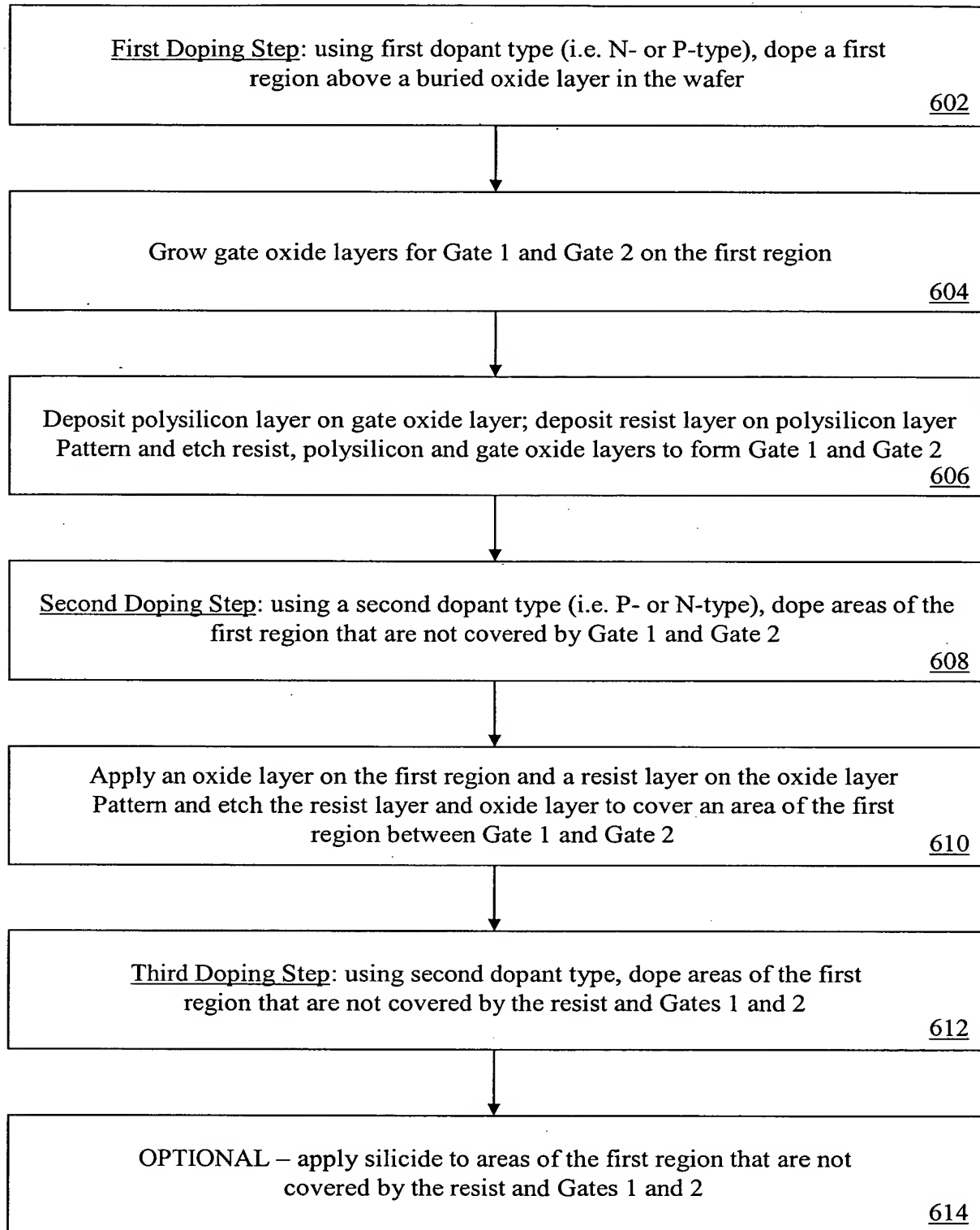


Fig. 6